

Ethernet Frame 311  
with IP Datagram as Data

Fig 3

FIGURE 1

High Freq.

Channel 403(0)

.

Channel 403(m)

Super Frame 404

Low Freq.

Super Packet 407(1)

407(n)

SPNR  
404

STRID415

STRID413

SPNR  
409

Data 411

RF  
Medium  
401

Data  
404  
on  
Channel  
403(1)

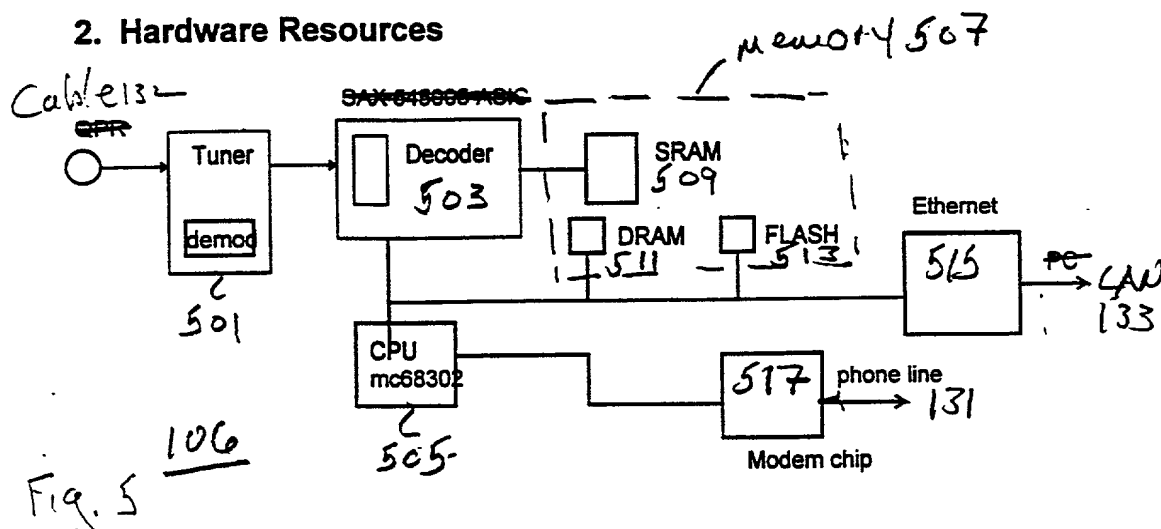
Fig 1

## 1. Introduction

This document describes the software characteristics of the cable modem used in Scientific Atlanta's *Cable Data Network Architecture*. The cable modem will provide asymmetrical transport using Quadrature Partial Response modulation downstream with upstream data provided by an integral telco-based modem. The connection to the host PC is provided by an Ethernet interface. Since the modem contains a telco modem, an additional mode of operation is as a standard AT command set modem when used with a Scientific Atlanta supplied device driver.

This modem will capitalize on several existing technologies already developed at Scientific Atlanta, most notably Digital Music Express (DMX), and SEGA product lines. In order to hasten the deployment of the modem, an operating system is being purchased from Microtec Research.

## 2. Hardware Resources



The Scientific Atlanta Cable Modem has several key hardware components that the software will accommodate. The three outside interfaces to the cable modem are the QPR/cable-TV coax, the ethernet port going out to the user network (probably a user PC), and the telephone company modem.

In order to support the three interfaces, there are a number of support components. On the cable /RF side of the box there is a tuner/demodulator which has been previously developed by SA for use in the SEGA project. This tuner/demodulator takes a QPR signal which arrives over a coaxial cable from the cable head end and produces a usable digital stream. This digital stream is passed to the SAX 545005 ASIC which digitally decodes the stream, which includes de-interleaving on multiple levels and decoding the BCH error correction encoding. For more information on how the SAX chip works, see the SA internal documentation titled "Design Requirements Document for the Sega ASIC for 32X-Cost Reduction (SAX-CR)".

In addition to the SAX chip the cable modem motherboard is equipped with a MC68302 general processor, which is connected to most of the other major components via a board level bus. This CPU will be running a real-time operating system called VRTX from Microtec Research. The CPU

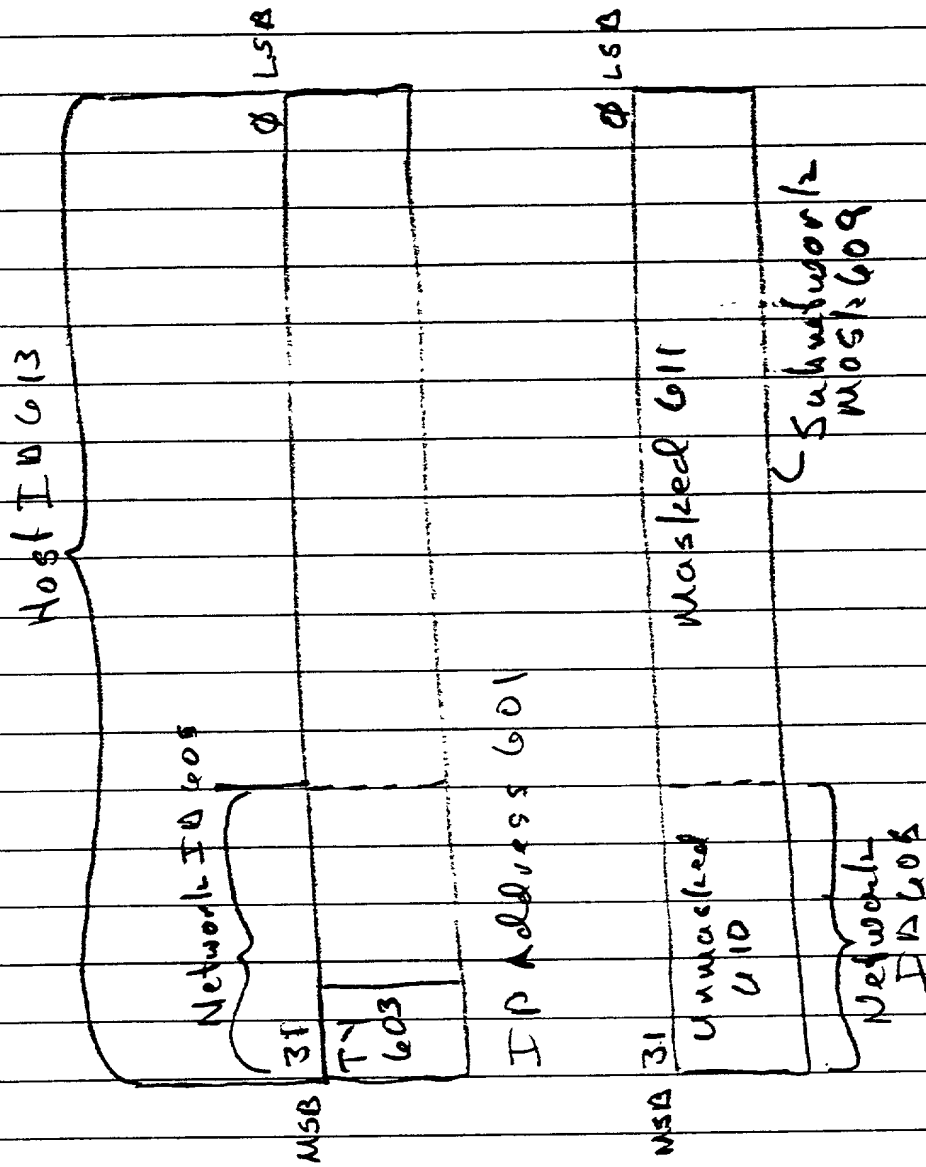


Fig. 6

Figure 11: Scenario D

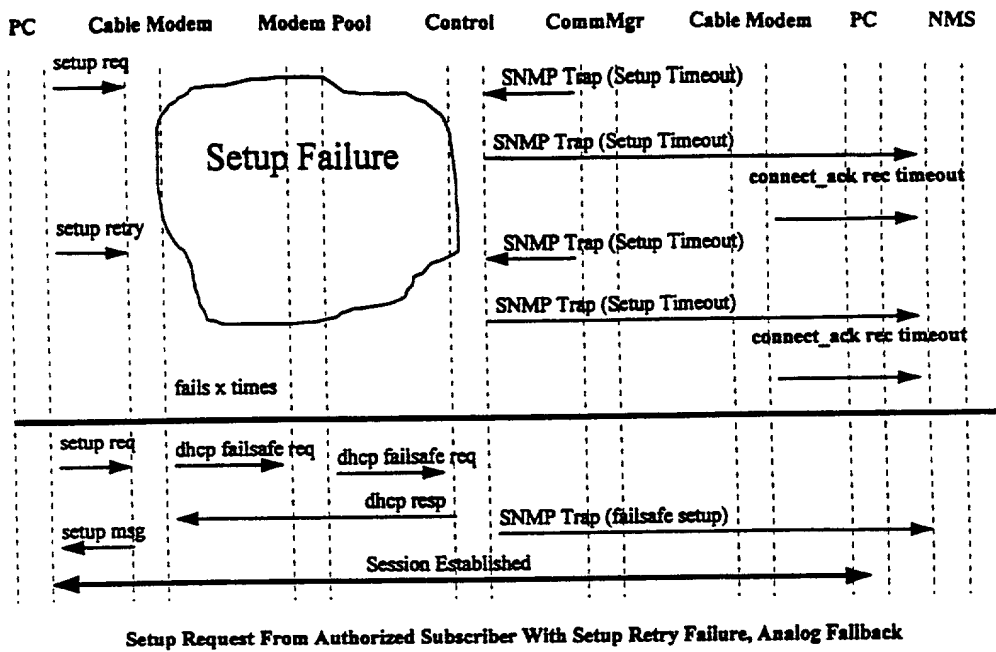
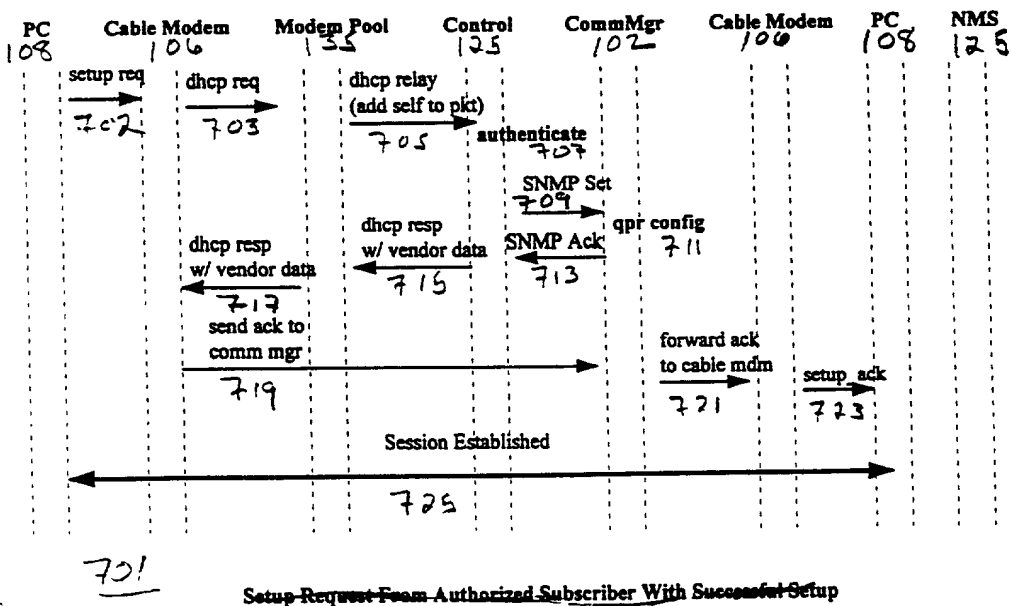


Figure 12: Scenario E



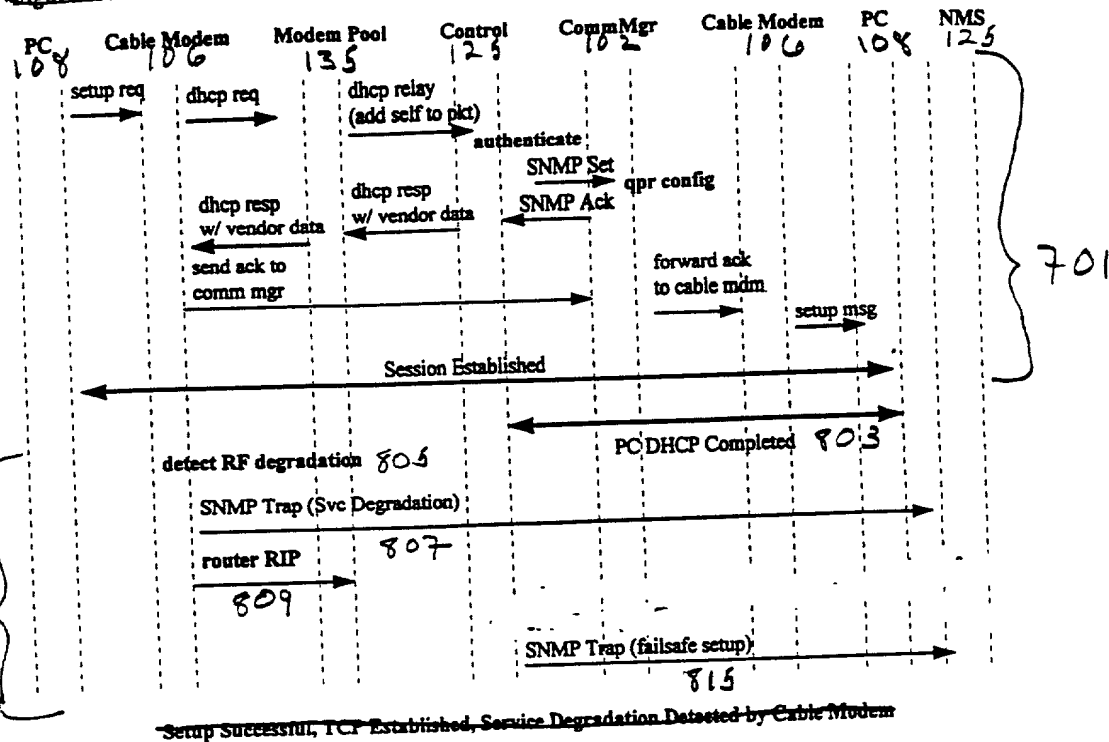
~~Figure 13 Scenario F~~

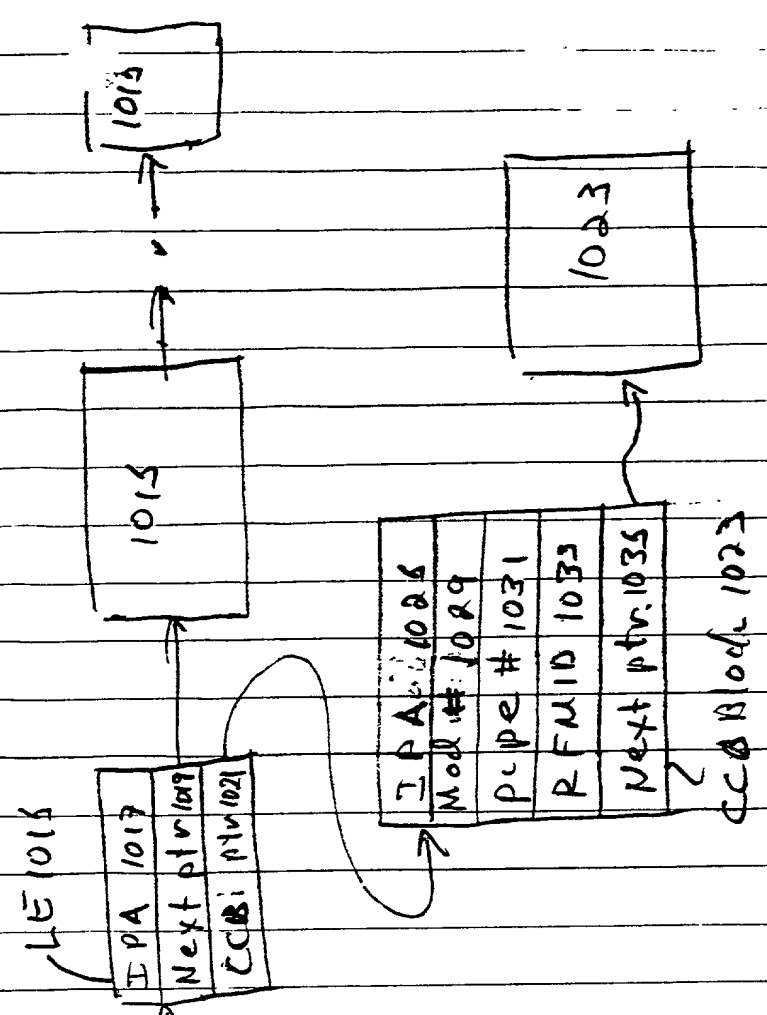
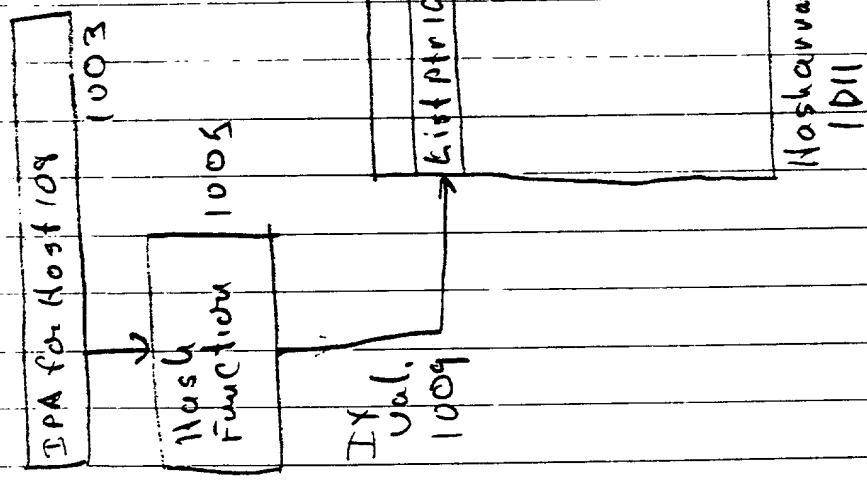
Fig. 8



	Dest IP	Gateway IP	Routing Info
903(c)	<u>906(c)</u> N/A <del>913</del> 915 917	IPA 203(c) 916	"
903(j)	N/A 918 919	IPA 214(c) 919	"
901	Routing table for Router		
911	Dest IP	Gateway IP	Routing Info
922(i)	<u>Host IP</u> 920	IPA 214(c) 931	"
922(j)	IPA 214(c) 930	IPA 214(c) 932	"
921	Routing table for Modem Pool		

1. 2. 3.

[illegible]



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Routing Info

Dest. IP	Gateway IP	Routing Info
Loop back IP 103	Loop back IP 103	
IP for Host 108 (104)	IP for Host 108 (101)	10133
108 (104)	108 (101)	1105
.	.	.
IP for Host 108 (101)	IP for Host 108 (101)	1105
Default 1115	Reusable IP for RF Module 106 (111)	1105

1103 (103)

1104

1103 (103)

Routing table 1101  
for Host 108 (104)

IP	LAN A
IP for Host 108 (101)	LAN A for Host 108 (101)
112 (101)	112 (101)
112 (101)	112 (101)
Reusable IP for RF Module 106 (111)	LAN A for RF Module 106 (111)
1117	1125
ARP Cache for Host 108 (104)	

112 (101)

112 2

